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[Q. 4.12: \(a\)Design a half-subtractor circuit with inputs x and y and outputs Diff and B](#)
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Q. 4.12: (a)Design a half-subtractor circuit with inputs x and y and outputs Diff and B The circ by Dr. Dhiman Kakati 11 months ago 12 minutes, 47 seconds 2,845 views Q. . (a), Design , a half-subtractor circuit with inputs x and y and outputs Diff and Bout. TH circuit subtracts the bits $x - y$ and ...

[Q. 4.14: Assume that the exclusive-OR gate has a propagation delay of 10 ns and that](#)
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Q. 4.14: Assume that the exclusive-OR gate has a propagation delay of 10 ns and that

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[Q. 3.23. Implement the following Boolean function F, together with the don't-care conditions d](#)

Q. 3.23. Implement the following Boolean function F, together with the don't-care conditions d by Dr. Dhiman Kakati 1 year ago 6 minutes, 46 seconds 3,635 views Q. 3 Implement the following Boolean function F, together with the don't-care conditions d using no more than two NOR gates: ...

[Q. 2.1: Demonstrate the validity of the following identities by means of truth tables: \(a\) DeMorgan'](#)

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